UNIVERSITY OF SOUTH FLORIDA

Major Research Area Paper Presentation

From Hardware to Algorithms: Securing the Next Ger
Machine Learning Applications
by
Brooks Olige

For the h.D.degree in Computer Scientegine ering

The costs of ratificial intelligence (AI) and machine learning (Mobilitinue to riseEnergy costs of building complex modelhave driven innovatiousingalternative hardware platforms lifted-programmable gate arrays (FPGAs) and tensor processing un(TEPUs) As standard computeparadigms for AIML shift away from generaburpose abrics so too has the discuisas on security of these systems earlie costs as a result dangerous ecurity threats have spurred research aid versarial machine leaton wards securing these applications and their hardware platform this talk, we discuss the curity risks of deploying ML applications from various cyberattack starting from the hardware abstract by the ML algorithm itself.

Tuesday, December 0721
11am 2pm
Online (Vicrosoffeam)s
THE PUBLIC IS INVITED

Examining Committee
Robert KaramPh.D., Major Professor
Srinivas KatkooriPh.D.
Mehran Mozaffari KermanPh.D.
Yasin YilmazPh.D.
JeanFrançois BiassPh.D.

Xinming Ou, Ph.D.

Disability Accommodations:

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